

-
- CH2
- TES 1.8 mA max
- FF bias 300 μ A max
- FF FB 300 μ A max
- SSA FB 300 μ A max
- SSA bias 300 μ A max
- SSA bias 300 μ A max
- SSA FB 300 μ A max
- FF FB 300 μ A max
- FF bias 300 μ A max
- TES 1.8 mA max
- Current sources
- DAC * 10
- 10 mirrors 1:1
- I²C * 10
- mirror 10 fanouts
- Iref
- LNA
- PS485/PS
- 500 μ m
- 6.8 mm x 3.8 mm
- AwaXe_v3**
- EARLY DM**

- Design using the  130nm
ST AwaXe_v4 to 6 in preparation of the EM

